

Index

A

A/D input, G-25
Acquisition clock, TTY setup, 3-6
AFC
 algorithms, 5-5
 analog, 2-15
 digital, from DAFC, 2-30
 introduction, 1-31
 TTY setup, 3-27
Angle
 input, 2-23
 output, 2-23
 S/D input, 2-24
Angle synchronization, introduction, 1-27
Angle syncing, LSYNC command, 6-41
Autocorrelations
 algorithms, 5-16
 introduction, 1-26
Automatic Frequency Control. *See* AFC
Azimuth angle, G-18 , G-19 , G-28

B

Back panel, G-8
BITE serial format, A-1 , A-5
Burst pulse, introduction, 1-22

C

Calibration, introduction, 1-11
CCOR threshold
 algorithms, 5-31
 qualifier, 5-33
CFGINTF command, 6-49
Chassis
 connector panel, 2-22
 direct connections, 2-21
 installation, 2-20
Clutter correction. *See* CCOR
Clutter filter
 LFILT command, 6-23

 microsuppression
 algorithms, 5-27
 TTY setup, 3-8
Clutter filtering, 5-17
Coax uplink
 installation, 2-17
 specification, 2-35
COHO, introduction, 1-14
Configuration examples
 dual polarization, 1-7
 Klystron, 1-6
 magnetron, 1-5
Connector panel, G-14
Control, introduction, 1-31
Corrected reflectivity, introduction, 1-30
Correction for Tx Power, algorithms, 5-9

D

DAFC
 CTI STALO example, 2-32
 description, 2-30
 drawings, pin out, G-33
 jumpers, 2-31
 MITEQ STALO example, 2-34
Debug Options, TTY setup, 3-33
DFT, 5-13
DFT/FFT, introduction, 1-29
Diagnostics
 introduction, 1-32
 TTY setup, 3-4
Differential phase. *See* PhiDP
Differential reflectivity. *See* ZDR
Digital receiver, introduction, 1-17
DspExport, 1-34
Dual polarization
 algorithms, B-1
 calibration, B-26
 dual channel receiver, B-5
 introduction, 1-7 , 1-30
 KDP calculation, B-14
 modes, B-6
 notation, B-6
 radar systems, B-3
 signal generator tests, 5-56
 standard moments, B-15

thresholding, B-25
Dual polarization , modes
 alternating dual channel, B-13
 alternating single channel, B-12
 fixed transmit, B-9
 simultaneous transmit, B-10
Dual PRF. *See* Velocity unfolding
Dual PRT. *See* Velocity unfolding

E

Elevation angle, G-21 , G-22 , G-28
ENDRAY_ output, introduction, 1-28
Environment, 1-44

F

Features
 digital transmitter, 1-1
 open hardware and software, 1-2
 frequency agility, 1-14
 I/Q LAN output, 1-3
 LAN connection, 1-3
 pulse compression, 1-14
 SoftPlane, 1-2
FFT, 5-13
 introduction, 1-29
Fiber optic downlink, installation, 2-17
FIFO, output, 6-1
FIR filter
 algorithms, 5-4
 TTY setup, 3-19
Front panel display, 2-26

G

Gaussian model filter, *see* GMAP, 5-20
Get processor parameters, GPARM command, 6-25
GMAP
 algorithm steps, 5-20
 benefits, 5-20
 configuration, 5-24
 example, 5-25
 model assumptions, 5-20
GPARM, command, 6-25

H

History, 1-1
Host computer interface
 complete command list, 6-1
 socket, 2-27

I

I/O-62
 connector panel
 drawings, G-14
 pin out, G-14
 connector panel, 2-22
 introduction, 1-15
I/Q, introduction, 1-3 , 1-30
IF
 bandwidth, 2-9
 frequency selection
 installation, 2-14
 TTY setup, 3-25
 IF to I/Q processing , 1-23
 saturation, 2-8
 signal processing, 1-21 , 5-4
IFD
 adjustment, 2-6
 drawings, G-30
 dynamic range, 2-9
 I/O connections, 2-5
 input power, 1-44 , 2-1 , 2-4
 installation, 2-2
 introduction, 1-9
 LED indicators, 2-6
 mounting, 2-4
 reference clock, 2-15
 revision history, 2-3
 sampling clock, 1-21
 signal level, 2-11
Initiate processing, PROC command, 6-14
Installation, test procedure, D-1
Interference Filter
 algorithms, 5-7
 CFGINTF command, 6-49
Intermediate frequency. *See* IF
IOTEST command, 6-11

K

KDP

algorithm. *See* Dual Polarization
description, B-1

Klystron

example, 1-20
introduction, 1-6

L

Large-signal linearization, algorithms, 5-9

LDR

algorithm. *See* Dual polarization
description, B-2

LDRNV command, 6-44

LEDs

on IFD, 2-6
SLED command, 6-43

LFILT command, 6-23

LOG filter, threshold qualifier, 5-33

LOG threshold, algorithms, 5-32

LRMSK command, 6-2

LSIMUL command, 6-35

LSYNC command, 6-41

M

Magnetron

example, 1-18
introduction, 1-5

Main chassis

back panel, G-8
drawings, G-2
general description, G-2
input power, 1-44 , 2-1 , 2-20 , G-7 , G-9
PC I/O, G-10
PCI cards, G-11

Mean velocity. *See* Velocity

Motherboard, introduction, 1-13

N

Network, socket interface, 2-27

Network interface, 1-34 , 1-35

Noise sample, SNOISE command, 6-12

NOP command, 6-2

O

Optional argument list, XARGS command,
6-46

OTEST command, 6-11

P

Phase control

output, 2-23
TTY setup, 3-35

PhiDP

algorithm. *See* Dual Polarization
description, B-1

Power requirements, 2-1 , 2-20 , G-7 , G-9

Power requirements, 1-44

Power-up, 2-26

PRF

limits, PWINFO command, 6-39
SETPWF command, 6-40
TTY setup, 3-14 , 3-19

PROC command, 6-14

Pulse compression, introduction, 1-14

Pulse pair, introduction, 1-29

Pulsewidth control

introduction, 1-31
PWINFO command, 6-39
SETPWF command, 6-40
TTY setup, 3-14

PWINFO command, 6-39

R

R2 processing, TTY setup, 3-8

Random phase

algorithms, 5-50
introduction, 1-30
TTY setup, 3-14

Range averaging

algorithms, 5-27
introduction, 1-27
LRMSK command, 6-3

Range mask, load command (LRMSK), 6-2

Range normalization, LDRNV command,
6-44

Range resolution, TTY setup, 3–19
Range unfolding, introduction, 1–30
RBACK command, 6–45
Real time TTY, TTY setup, 3–34
Reflectivity
 algorithms, 5–27
 introduction, 1–30
Reflectivity calibration, 5–39
Relays, G–23
Reliability, 1–44
RESET command, 6–37
RhoHV, description, B–2
ROM revision, TTY setup, 3–3
Roundtrip delay, TTY setup, 3–4
RVP8/Rx card, introduction, 1–10
RVP8/Tx card, introduction, 1–13

S

S/D input, G–28
Saturation headroom, TTY setup, 3–9
SBC, introduction, 1–13
Self tests, 1–32
Serial status formats, A–1
SETPWF command, 6–40
SIG threshold, algorithms, 5–32
Simulations
 burst pulse, TTY setup, 3–31
 LSIMUL command, 6–35
 output data, TTY setup, 3–33
SLED command, 6–43
SNOISE command, 6–12
Socket interface, 1–34 , 1–35 , 2–27
Software, support utilities, 1–33
SOPRM command, 6–4
Speckle filter
 algorithms, 5–35
 introduction, 1–28
Spectrum Processing
 clutter , 5–17
 window, 5–13
Spectrum processing, 5–13
Spectrum width
 algorithms, 5–29
 introduction, 1–30

SQI threshold
 algorithms, 5–30
 qualifier, 5–33
STAR, B–10
Synchro input, G–28

T

TAG lines
 introduction, 1–27
 TTY setup, 3–10
Test point, output , 2–24
Testing
 test procedure, D–1
 with signal generator, 5–55
Thresholding
 algorithms, 5–33
 adjusting, 5–34
 introduction, 1–28
Time series, algorithms, 5–11
Trigger
 blanking, 1–31
 external input, TTY setup, 3–14
 external pre-trigger, 2–22
 input, 2–25
 installation, 2–22 , 2–25
 introduction, 1–31
 output, 2–25
 outputs, TRIGWF command, 6–37
 TTY setup, 3–14
 waveform, example of Dual PRF, 5–49
TRIGWF command, 6–37
TTY setup
 complete listing, 3–1
 M+ – debug options, 3–33
 Mb – burst pulse and AFC, 3–25
 Mc – top level configuration, 3–6
 Mf – clutter filters, 3–12
 Mp – processing options, 3–8
 Mt – general trigger setups, 3–14
 Mz – transmissions modulations, 3–35
 P+ – plot test pattern, 4–2
 Pb – plot burst pulse timing, 4–5
 Pr – plot receiver waveforms, 4–22
 Ps – plot burst spectra and AFC, 4–10
 TTYOP command, 6–43

V – view internal status, 3–3
TTYOP command, 6–43
TWT, example, 1–20

U

Uncorrected reflectivity, introduction, 1–30
Unfolding, introduction. *See* Velocity unfolding

V

Velocity
 algorithms, 5–29
 introduction, 1–30
Velocity unfolding
 algorithms, 5–44 , 5–46

introduction, 1–28

W

Weather signal processing, introduction, 1–25

WSP threshold, qualifier, 5–33

X

XARGS command, 6–46

Z

ZDR
 algorithm. *See* Dual polarization
 description, B–1